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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/010,895	11/09/2001	Gurtej S. Sandhu	98-1191.01	8300
75	90 06/02/2005		EXAM	INER
Charles Brantl			UMEZ ERONIN	II, LYNETTE T
Micron Technology, Inc. 8000 S Federal Way		ART UNIT	PAPER NUMBER	
Boise, ID 83716			1765	
			DATE MAILED: 06/02/200:	5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/010,895	SANDHU, GURTEJ S.			
		Examiner	Art Unit			
		Lynette T. Umez-Eronini	1765			
Period fo	The MAILING DATE of this communication apported in the plant of the plant is a second of the	pears on the cover sheet with the c	orrespondence address			
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a repl period for reply is specified above, the maximum statutory period are to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailined patent term adjustment. See 37 CFR 1.704(b).	. 136(a). In no event, however, may a reply be tirely within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE.	nely filed s will be considered timely. the mailing date of this communication. (D) (35 U.S.C. § 133).			
Status						
1) 🛛	Responsive to communication(s) filed on 14 N	March 2005.				
	This action is FINAL . 2b) This action is non-final.					
3)□	<u> </u>					
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
5)⊠ 6)⊠ 7)□	Claim(s) 59-65,69 and 70 is/are pending in the 4a) Of the above claim(s) is/are withdra Claim(s) 59-65 is/are allowed. Claim(s) 69 and 70 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/o	wn from consideration.				
Applicat	ion Papers					
10)⊠	The specification is objected to by the Examine The drawing(s) filed on 11/9/2001 is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Examine The specific and the	accepted or b) objected to by drawing(s) be held in abeyance. Setion is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Prioritν ι	under 35 U.S.C. § 119					
12)□ a)(Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bureasee the attached detailed Office action for a list	ts have been received. ts have been received in Applicationity documents have been receive u (PCT Rule 17.2(a)).	ion No ed in this National Stage			
2) 🔲 Notic 3) 🔲 Inforr	t(s) se of References Cited (PTO-892) se of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) sr No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D: 5) Notice of Informal F 6) Other:				

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DETAILED ACTION

Priority

1. An application in which the benefits of an earlier application are desired must contain a specific reference to the prior application(s) in the first sentence(s) of the specification or in an application data sheet by identifying the prior application by application number (37 CFR 1.78(a)(2) and (a)(5)). If the prior application is a non-provisional application, the specific reference must also include the relationship (i.e., continuation, divisional, or continuation-in-part) between the applications except when the reference is to a prior application of a CPA assigned the same application number.

It is suggested to update the current status of the present application.

Specification

2. The use of the trademark "Flowfill" has been noted in this application. It should be capitalized wherever it appears and be accompanied by the generic terminology.

Although the use of trademarks is permissible in patent applications, the proprietary nature of the marks should be respected and every effort made to prevent their use in any manner which might adversely affect their validity as trademarks.

Appropriate correction is required.

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Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 5. Claim 69 is rejected under 35 U.S.C. 103(a) as being unpatentable over Pan (US 5,897,364) in view of Kwon et al. (US 5,796,133) and Havemann (US 5,565,384).

Pan teaches depositing a BSG layer **24** over a semiconductor, which has a gate electrode **16** and dielectric side walls **22** (same as applicant's insulation layer), depositing a PSG layer **28** on the BSG layer **24** (column 2, lines 12-43), and depositing a passivation layer **38**, which complete the fabrication of the integrated circuit device (column 3, lines 48-49). The aforementioned reads on,

A method of depositing an interlayer dielectric, comprising:

providing a first level of a semiconductor device, said first level defining a topography and comprising insulation;

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depositing BSG onto discrete portions of said topography;

providing a second level of said semiconductor device over said BSG; and providing a second level of said semiconductor device over said BSG.

Pan differs in failing to teach said BSG having a dielectric constant of at most 3.

Kwon teaches a low dielectric material comprising BSG (column 4, lines 27-34).

Havemann teaches, ". . . Silicon dioxide has a dielectric constant of about 3.9. This constant is based on a scale where 1.0 represents the dielectric constant of a vacuum. Various materials exhibit dielectric constants from very near 1.0 to values in the hundreds. As used herein, the term low-k will refer to a material with a dielectric constant less than 3.5" (column 1, lines 56-64). Havemann also teaches low-k dielectric materials are used as a gap filler between horizontally adjacent conductors, thereby decreasing line-to-line capacitance (column 2, lines 31-36). The above reads on BSG having a dielectric constant of at most 3.

Since it is known that low k dielectric material insulate conductive material to decrease the capacitance between adjacent conductors (Havemann, column 2, lines 31-36), BSG is a low k material and low k materials have dielectric constant less than 3.5, then it would have been obvious to one having ordinary skill in the art to modify Pan by combining the teachings of Kwon and Havemann in employing a low k material such BSG, for the purpose of decreasing the capacitance between neighboring conducting layers.

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Allowable Subject Matter

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6. Claims 59-65 are allowed.

7. The following is a statement of reasons for the indication of allowable subject matter: As to claims 59-65, the prior art of record taken alone or in combination fail to suggest, teach, or render obvious a method of forming oxide over a transistor gate and over a substrate extending laterally from under said gate, comprising: stopping said removal of a portion of said insulation with said second oxide, in combination with the

rest of the limitations of the said claims.

Response to Arguments

8. Applicant's arguments filed 3/14/2005 have been fully considered but they are not persuasive. Applicant traversed the rejection of claims 22-42, 66-68, and 71-78 and would address the patentability of the said claims in a related application and further request these claimed be cancelled without prejudice. Applicant further argued not finding a basis for rejection claims 69-70. Applicant's arguments do not comply with 37 CFR 1.111(c) because they do not clearly point out the patentable novelty which he or she thinks the claims present in view of the state of the art disclosed by the references cited or the objections made. Further, they do not show how the amendments avoid such references or objections.

Conclusion

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9. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lynette T. Umez-Eronini whose telephone number is 571-272-1470. The examiner is normally unavailable on the First Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nadine Norton can be reached on 571-272-1465. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only.

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For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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May 26, 2005

SUPERVISORY PATENT EXAMINER